


Form PTO-1449 (Modified)  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  	Application No.	09/814,417	
	Filing Date	March 21, 2001	
	First Named Inventor	Subbarao, Prasad	
	Group Art Unit	2128	
	Examiner Name	FREJD	
Sheet 1 of 1		Attorney Docket No.	00-077 68513

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U.S. PATENT DOCUMENTS								
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EXAMINER INITIALS*	CITE NO.	COPY NOT ENCLOSED PER 37 CFR § 1.98(d)	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
RF	AA		FLORENTIN DARTU et al.; "Performance Computation for Precharacterized CMOS Gates with RC Loads"; IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 5, May 1996; pp. 544-553.

Examiner Signature	RUSSELL FREJD	Date Considered	6.8.04
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\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.